

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re: Shin-Ae Lee et al.

Serial No.: 10/683,782

Filing Date: October 10, 2003

For: **MOS TRANSISTORS HAVING INVERTED T-SHAPED GATE
ELECTRODES AND FABRICATION METHODS THEREOF**

Confirmation No.: 1793

Group Art Unit: 2813

Examiner: Jack S. J. Chen

Date: August 24, 2006

Mail Stop Issue Fee
Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

INTERVIEW SUMMARY

Sir:

Examiner Chen telephoned Applicants' representative on August 14, 2006 to request permission to cancel withdrawn Claims 10-11, which would place the application in condition for allowance. Applicants' representative agreed to the cancellation of Claims 10-11. No other matters were discussed.

Respectfully submitted,



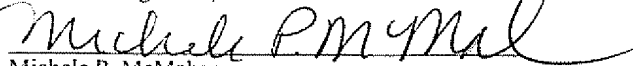
D. Randal Ayers
Registration No. 40,493
Attorney for Applicants

Customer Number 20792

Myers Bigel Sibley & Sajovec, P.A.
P.O. Box 37428
Raleigh, NC 27627
919-854-1400
919-854-1401 (Fax)

CERTIFICATION OF ELECTRONIC TRANSMISSION UNDER 37 CFR § 1.8

I hereby certify that this correspondence is being transmitted electronically to the U.S. Patent and Trademark Office on August 24, 2006.



Michele P. McMahan

Date of Signature: August 24, 2006